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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/955,885	09/19/2001	Yasutoshi Hirano	2271/65888	9849

7590 08/09/2004

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EXAMINER
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TRAN, DENISE

ART UNIT	PAPER NUMBER
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2186

DATE MAILED: 08/09/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

**Advisory Action**

Application No.

09/955,885

Applicant(s)

HIRANO, YASUTOSHI

Examiner

Denise Tran

Art Unit

2186

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 15 July 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY [check either a) or b)]**

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.  
b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.  
2. ☐ The proposed amendment(s) will not be entered because:  
(a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);  
(b) ☐ they raise the issue of new matter (see Note below);  
(c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or  
(d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_

3. ☐ Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.  
4. ☐ Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).  
5. ☒ The a) ☐ affidavit, b) ☐ exhibit, or c) ☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.  
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.  
7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☐ will not be entered or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: none.Claim(s) objected to: none.Claim(s) rejected: 1-14.

Claim(s) withdrawn from consideration: \_\_\_\_\_.

8. ☐ The drawing correction filed on \_\_\_\_\_ is a) ☐ approved or b) ☐ disapproved by the Examiner.  
9. ☐ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). \_\_\_\_\_.  
10. ☐ Other: \_\_\_\_\_

**BEST AVAILABLE COPY***Denise Tran*  
*8/4/04*

Continuation of 5. does NOT place the application in condition for allowance because: prior art still render the claims unpatentable and the final rejection is deemed to be proper with respect to applicant's amendment filed 2/9/04. In addition, the combination of Pawate and Hudson with respect to claims 1-7 or the combination of Nakajima, Pawate, and Hudson with respect to claims 8-14 teaches all the claimed limitations. Pawate shows a clock signal control part controlling outputting of the clock signal to the digital signal processor so that the programs stored in the external memory part can be forwarded to the internal memory part (e.g. col. 13, line 59 to col. 14, line 4 and figure 2, elements 110-140, 180). Hudson shows the use of wherein the control of output of a signal is performed without requiring reinitialization of the digital signal processor (e.g. col. 18, line 53 to col. 19, line 40; col. 14, lines 47-68). In addition, Hudson shows the use of placing the individual subsystems in low power mode and halt mode which permits the subsystem to operate at a lower frequency, utilizing the lower frequency clock (e.g. col. 14, lines 47-68).

*Deussen*  
8/4/04

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